

Electronic Acknowledgement Receipt

EFS ID:	2013671
Application Number:	10595303
International Application Number:	
Confirmation Number:	7114
Title of Invention:	Method and Device For Secure, Insulated and Electrically Conductive Assembling of Treated Semiconductor Wafers
First Named Inventor/Applicant Name:	Roy Knechtel
Customer Number:	21967
Filer:	Rodger L. Tate/Mildred Adesida
Filer Authorized By:	Rodger L. Tate
Attorney Docket Number:	60291.000048
Receipt Date:	26-JUL-2007
Filing Date:	
Time Stamp:	12:31:02
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for status of Application	DOC003.PDF	91522 b2ea36d55f1dc0710017bb5a906ea79f0acabbed	no	1

Warnings:

Information:		
<table border="1"><tr><td>Total Files Size (in bytes):</td><td>91522</td></tr></table>	Total Files Size (in bytes):	91522
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